

Figure 1
System

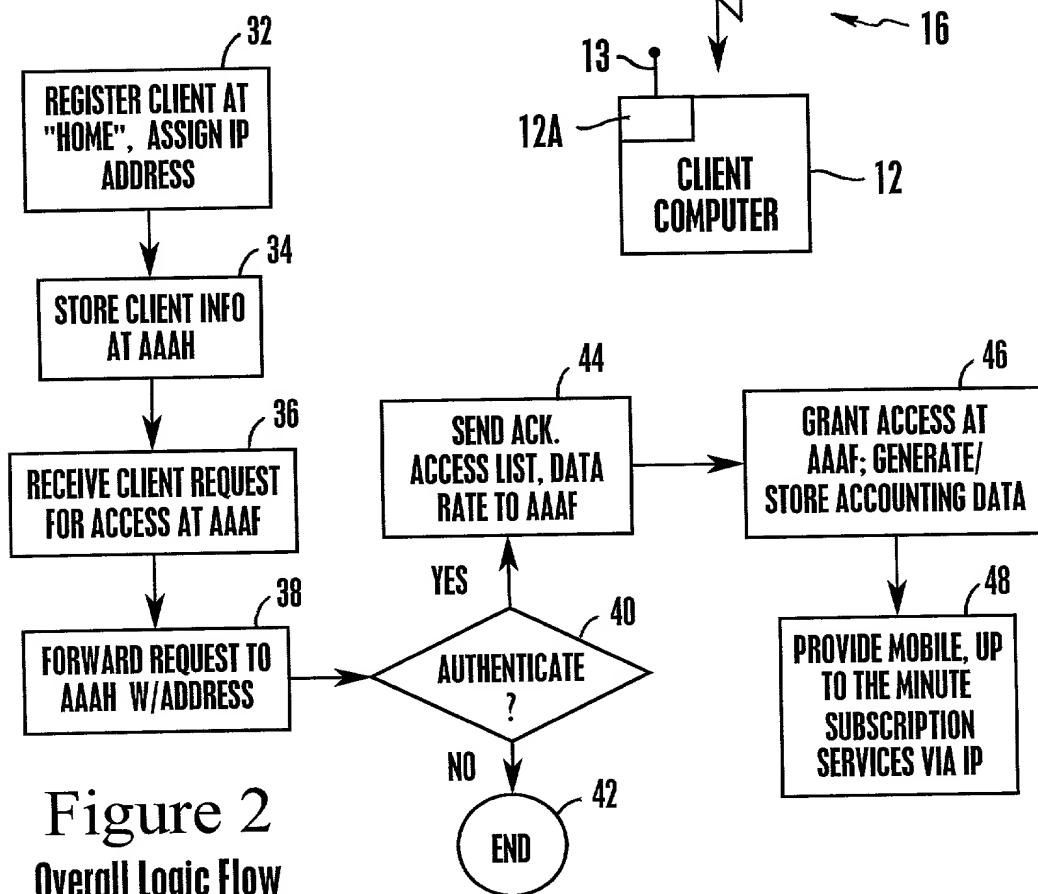


Figure 2
Overall Logic Flow

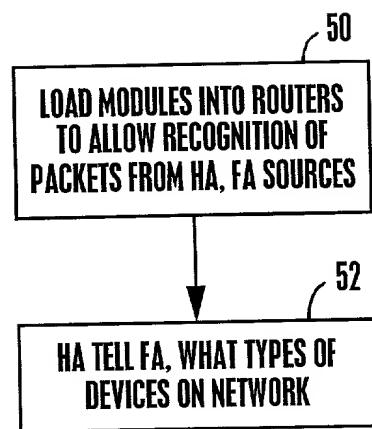


Figure 3
Set-up logic

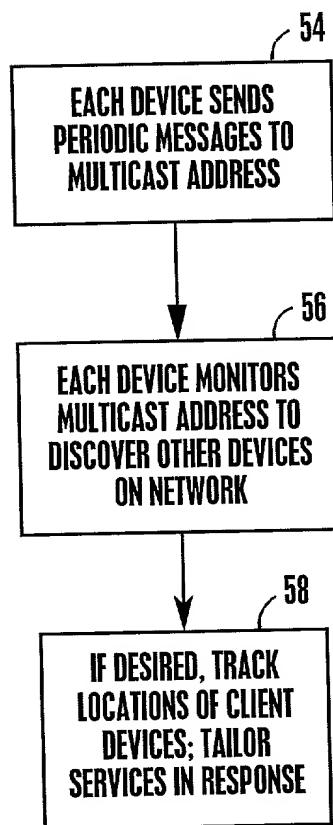


Figure 4
Operation logic